



Title: SELF-ALIGNED TRIPLE GATE SILICON-ON-INSULATOR (SOI) DEVICE  
Inventors: [redacted] yokapic et al.  
Appl No.: [redacted] 11,445  
Docket No.: AMDI-08399US0  
Attorney: Stephen R. Bachmann  
Filing Date: November 13  
Art Unit: 2811  
Telephone No.: 415-362-3800

FIG. 4A

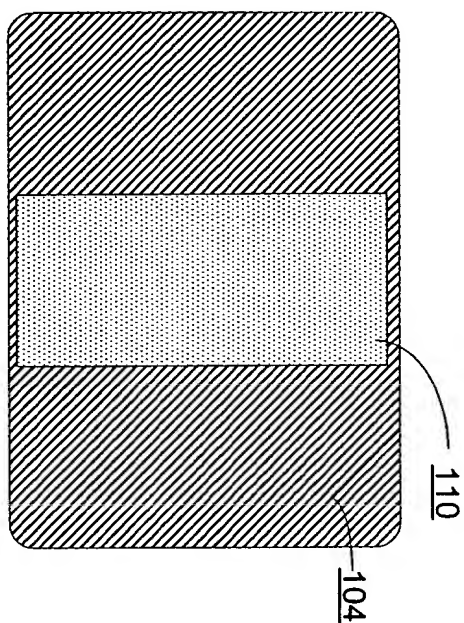


FIG. 4B

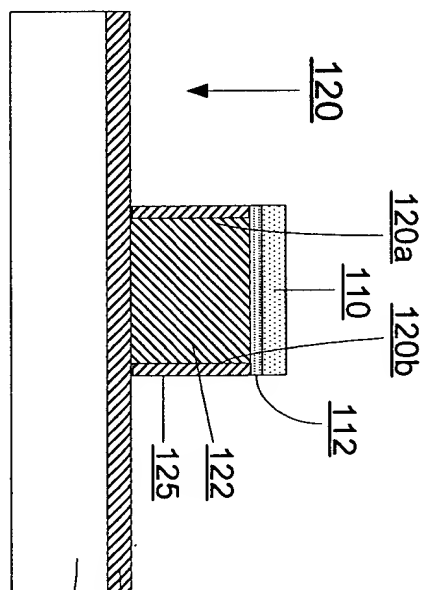
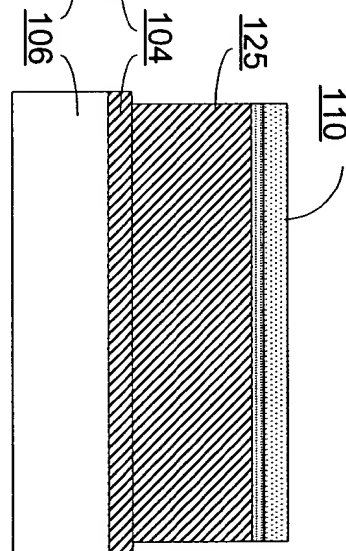


FIG. 4C



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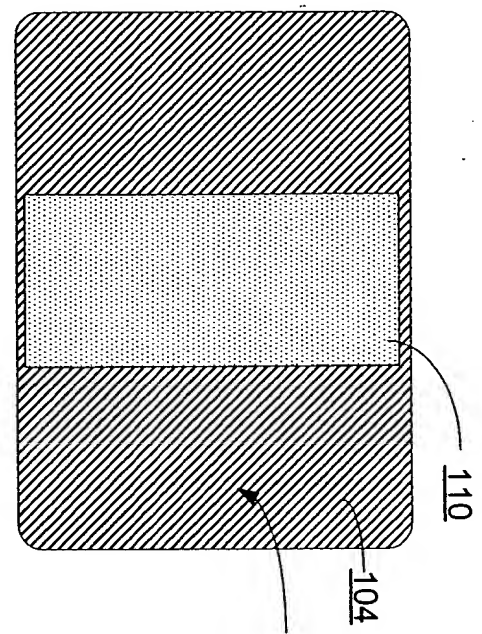


FIG. 5A

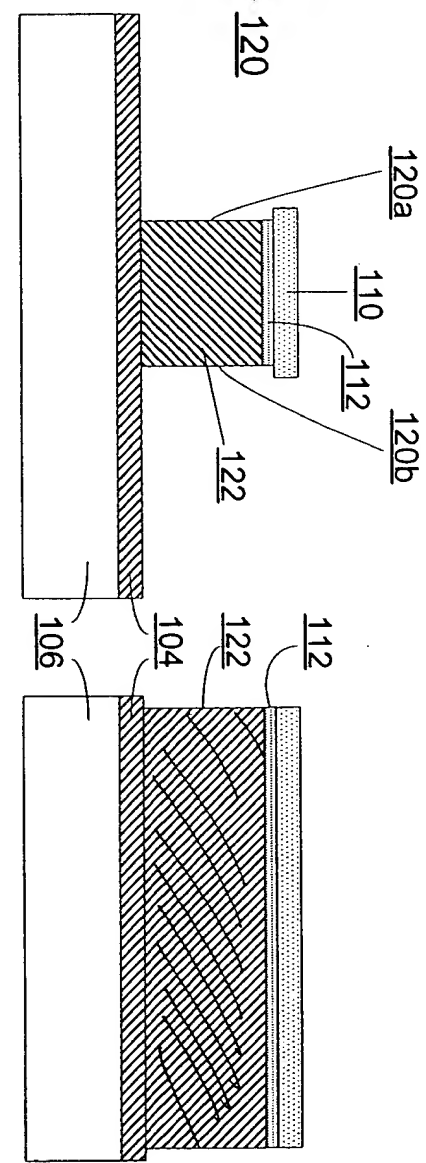


FIG. 5B

FIG. 5C

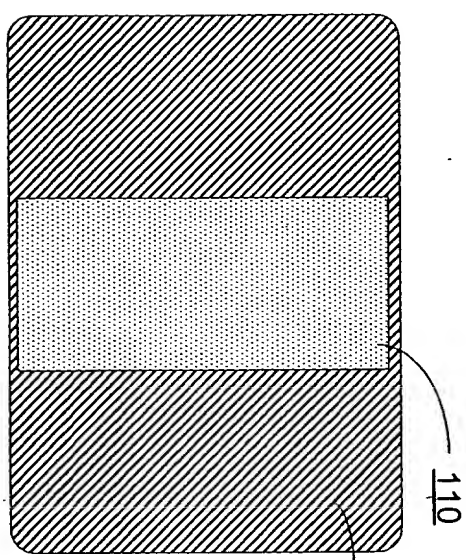


FIG. 6A

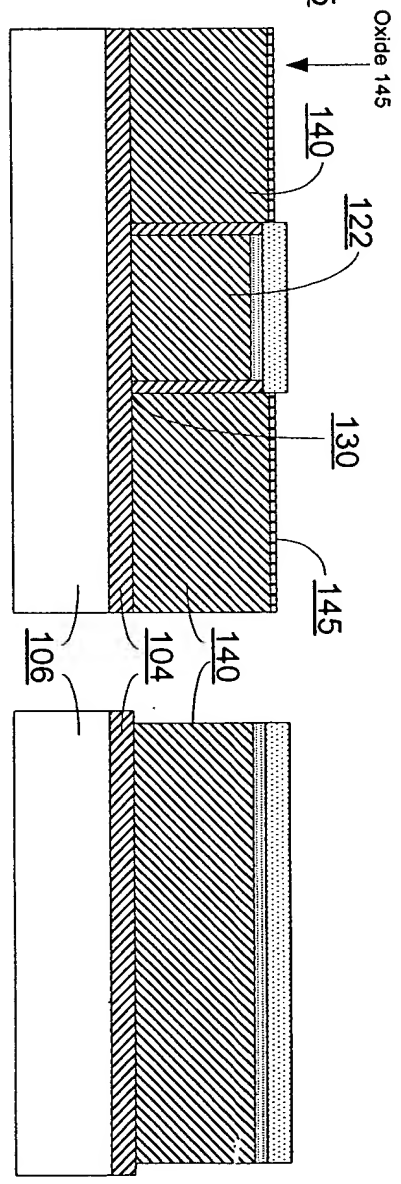


FIG. 6B

FIG. 6C

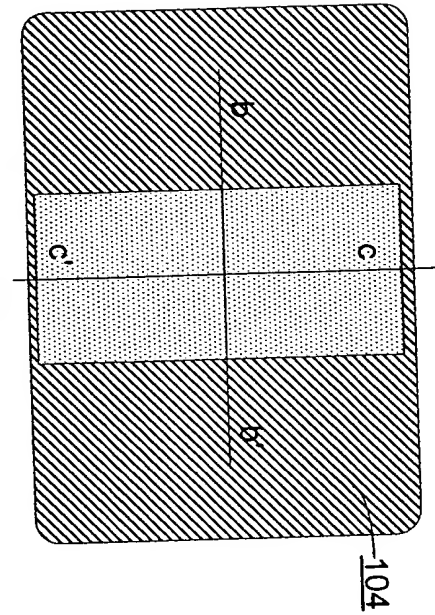


FIG. 12A

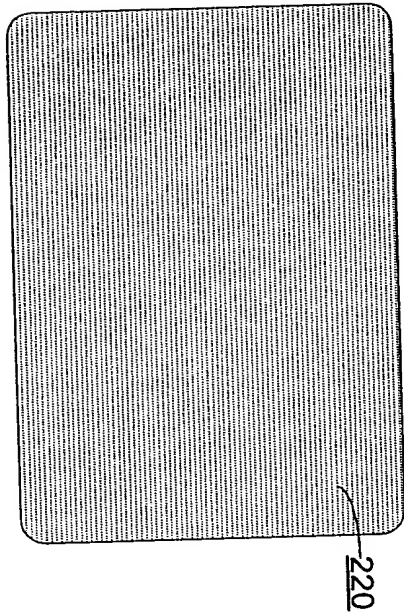


FIG. 13A

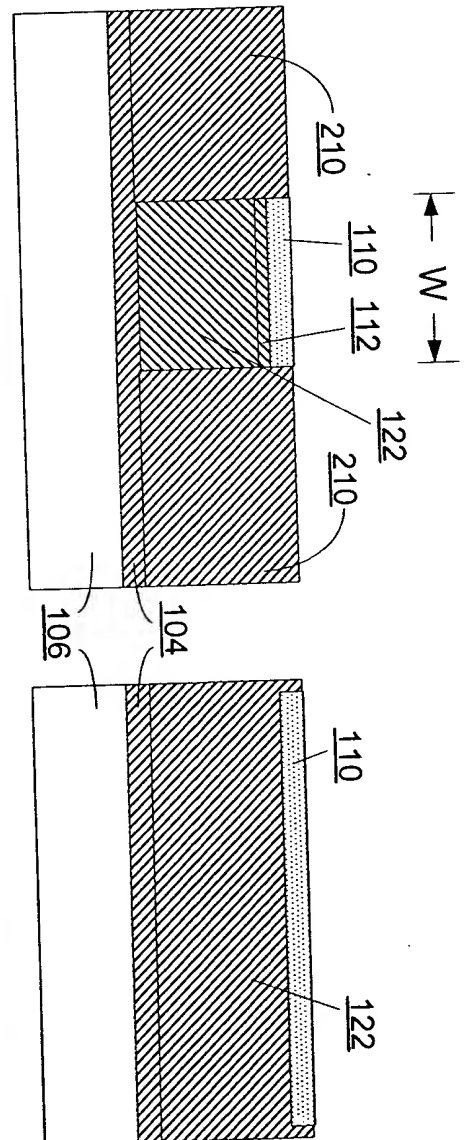


FIG. 12B

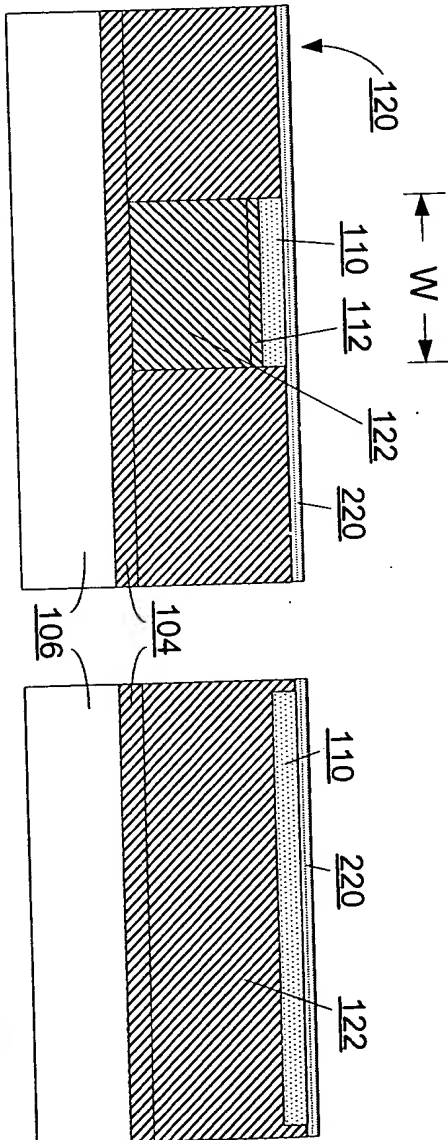


FIG. 13B

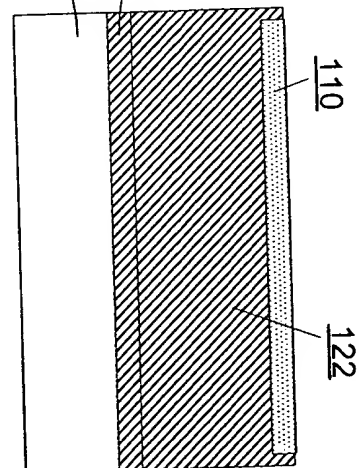


FIG. 12C

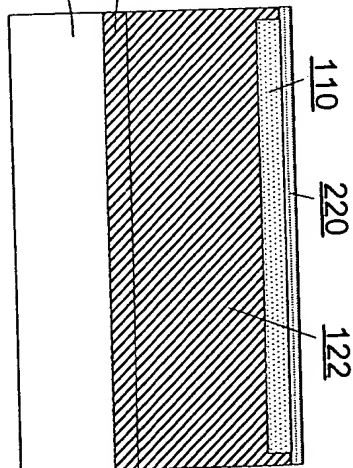


FIG. 13C

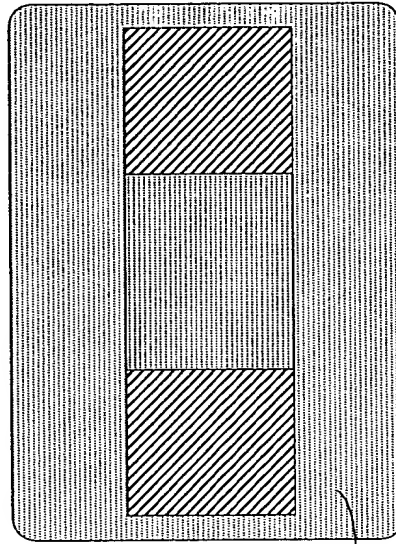


FIG. 14A

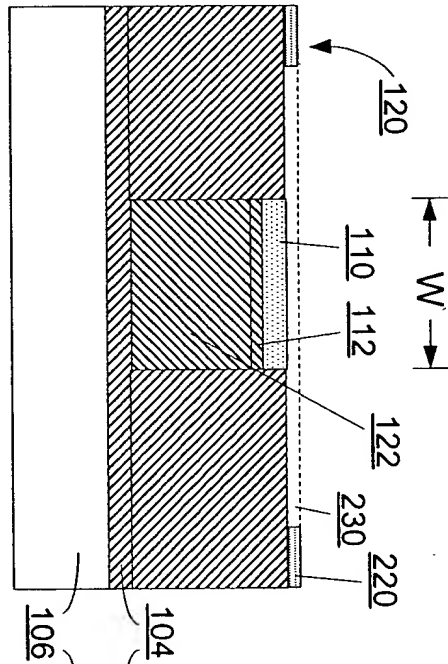


FIG. 14B

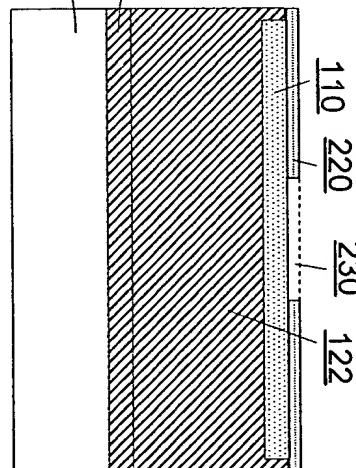


FIG. 14C



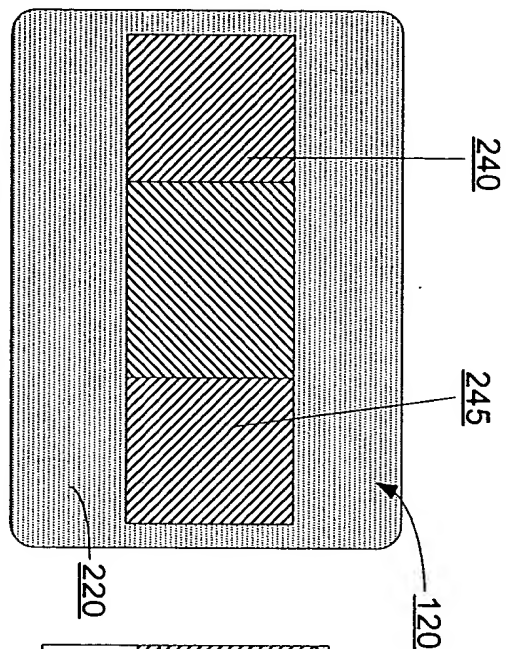


FIG. 15A

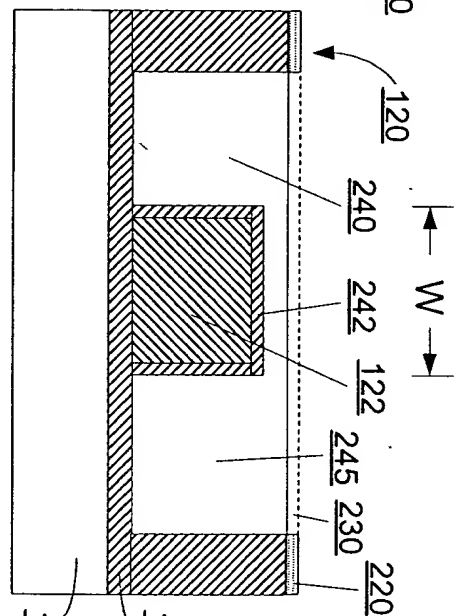


FIG. 15B

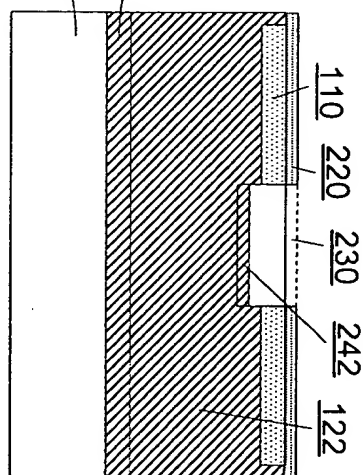


FIG. 15C

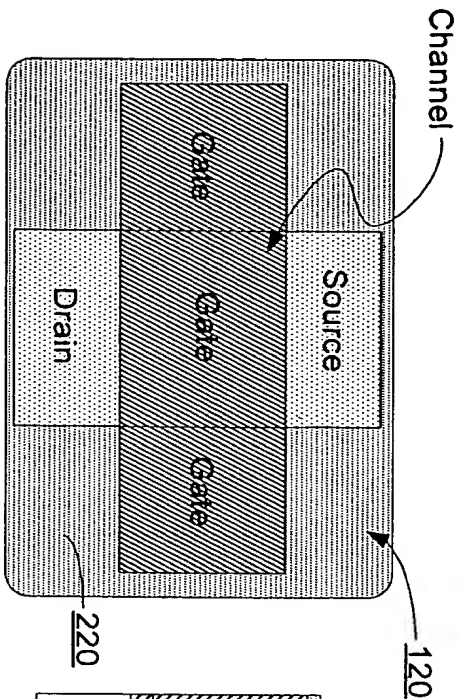


FIG. 16A

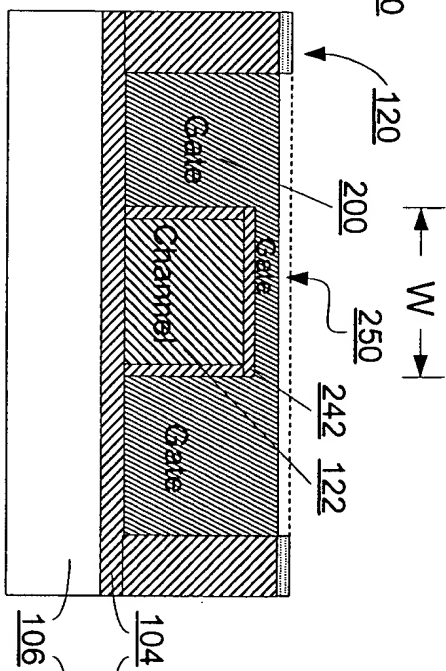


FIG. 15B

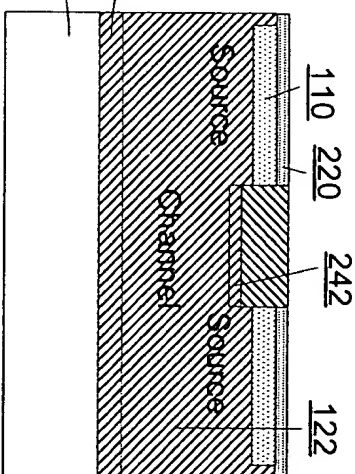


FIG. 15C



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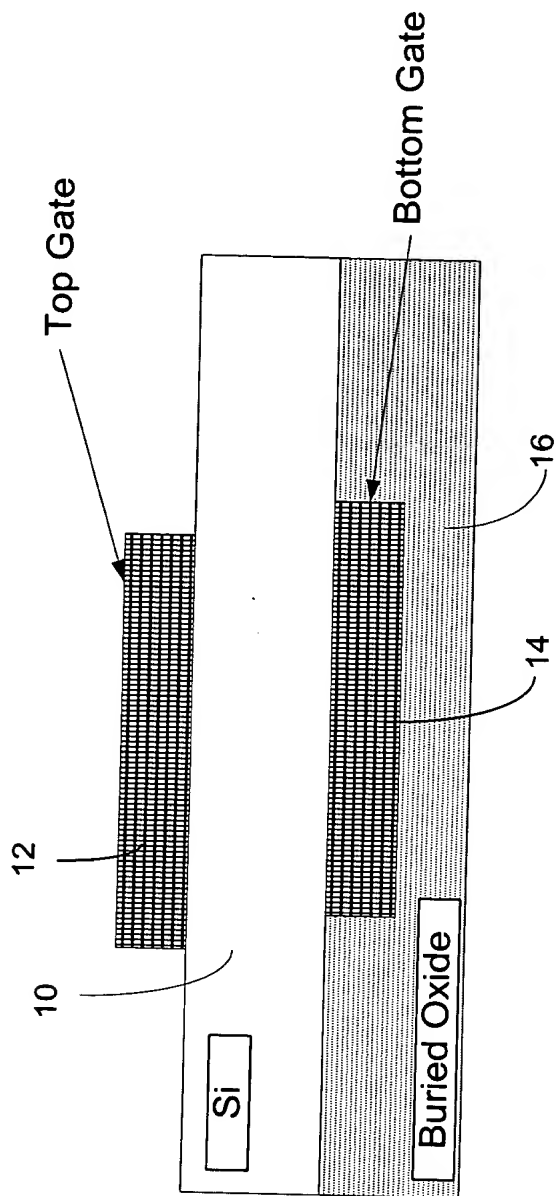


FIG. 1